

10/807,692

EAST History

7/14/2005

4:39 PM

BRS	L1	4509	(exclusive adj nor) or (exclusive-nor)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT;	14-Jul-05
BRS	L2	4178	1 and inputs	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT;	14-Jul-05
BRS	L3	1829	1 and NAND	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT;	14-Jul-05
BRS	L4	1829	3 and NOR	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT;	14-Jul-05
BRS	L5	1814	4 and outputs	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT;	14-Jul-05
IS&R	L6	96	(326/54).CCLS.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT;	14-Jul-05
BRS	L7	17	5 and 6	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT;	14-Jul-05
BRS	L8	881	5 and semiconductor	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT;	14-Jul-05
IS&R	L9	90347	((("326") or ("345"))).CLAS.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT;	14-Jul-05
BRS	L10	213	4 and 9	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT;	14-Jul-05
IS&R	L11	67465	("345").CLAS.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT;	14-Jul-05
BRS	L12	79	1 and 11	USPAT	14-Jul-05
BRS	L13	171	inspecting adj circuit	USPAT	14-Jul-05
BRS	L14	1712	checking adj circuit	USPAT	14-Jul-05
BRS	L15	1881	13 or 14	USPAT	14-Jul-05
BRS	L16	885	inspecting adj circuit	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT;	14-Jul-05
BRS	L17	4998	checking adj circuit	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT;	14-Jul-05

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BRS L18 5881 16 or 17

US-PGPUB; USPAT; 14-Jul-05
USOCR; EPO; JPO;
DERWENT;

BRS L19 48 1 and 18

US-PGPUB; USPAT; 14-Jul-05
USOCR; EPO; JPO;
DERWENT;